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October 27, 2003

Attorney Docket No.: 12732-097002

## Mail Stop Patent Application

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Presented for filing is a new divisional patent application of:

Applicant: SHUNPEI YAMAZAKI, HIDEOMI SUZAWA, KOJI ONO and  
YOSHIHIRO KUSUYAMA

Title: SEMICONDUCTOR DEVICE AND FABRICATION METHOD  
THEREOF

The prior application is assigned of record to Semiconductor Energy Laboratory Co., Ltd., a Japan corporation, by virtue of an assignment submitted to the Patent and Trademark Office and recorded on March 26, 2002 at Reel 012741, Frame 0667.

Enclosed are the following papers, including those required to receive a filing date under 37 CFR §1.53(b):

	<u>Pages</u>
Specification	40
Claims	8
Abstract	1
Declaration	3
Drawing(s)	14

### Enclosures:

- Serial number card (green).
- Preliminary amendment and appendix, 11 pages.
- Transmittal of formal drawings (1 page) and formal drawings (Figs. 1A-14C, 14 sheets).
- Information disclosure statement, 1 page.  
PTO-1449, 2 pages.
- Copy of transmittal of priority document (2 pages), which was filed in the parent application. Please enter in this application.
- Copy of notice of recordation of assignment document as received in parent (4 pages). Please enter in this application.

17707 U.S. PTO  
102703  
Frederick P. Fish  
1855-1930  
W.K. Richardson  
1859-1951



BOSTON  
DALLAS  
DELAWARE  
NEW YORK  
SAN DIEGO  
SILICON VALLEY  
TWIN CITIES  
WASHINGTON, DC

15757 U.S. PTO  
10/693491  
10/27/03

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This application is a divisional of U.S. application serial no. 10/105,282, filed March 26, 2002, which, under 35 USC 119, claims the benefit of a foreign priority application filed on March 27, 2001 in Japan as application number 2001-091275. This application claims priority to both of these prior applications, and the disclosures of the prior applications are considered part of (and are incorporated by reference in) the disclosure of this application.

Basic filing fee	\$770
Total claims in excess of 20 times \$18	\$0
Independent claims in excess of 3 times \$84	\$0
Fee for multiple dependent claims	\$0
Total filing fee:	\$770

A check for the filing fee is enclosed. Please apply any other required fees or any credits to deposit account 06-1050, referencing the attorney docket number shown above.

If this application is found to be incomplete, or if a telephone conference would otherwise be helpful, please call the undersigned at (202) 783-5070.

Kindly acknowledge receipt of this application by returning the enclosed postcard.

Please direct all correspondence to the following:

26171

PTO Customer Number

Respectfully submitted,



William G. Hughes, Jr.  
Reg. No. 46,112

**Enclosures**

JFH/aks

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki et al.                      Art Unit : Unknown  
Serial No. : New Divisional Application                      Examiner : Unknown  
Filed : October 27, 2003  
Title : SEMICONDUCTOR DEVICE AND FABRICATION METHOD THEREOF

**Attention: Official Draftsman**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450


**TRANSMITTAL OF FORMAL DRAWINGS**

Please substitute the enclosed fourteen (14) sheets of formal drawings (Figs. 1A-14C) for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: October 27, 2003

  
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